

ABSTRACT

A voltage level translator provides an output signal having an external voltage in response to an input signal having an internal voltage. The voltage level translator
5 includes first and second input signal transistors, first and second output signal transistors, and includes a signal stabilization circuit and/or an enable circuit. A ready-signal generation circuit provides a ready signal indicating that a voltage supply is at an operating voltage. The ready-signal generation circuit can include unbalanced transistors.

10